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Amend D w/ KCO  
if Kalyan  
5/7/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

Tirdad SOWLATI et al.

Group Art Unit: 2811

Serial No.: 09/545,785

Examiner: Ori NADAV

Filed: 7 April 2000

Title: INTERDIGITATED  
MULTILAYER CAPACITOR  
STRUCTURE FOR DEEP SUB-  
MICRON CMOS

RECEIVED  
APR 23 2003  
TC 2800 MAIL ROOM

**AMENDMENT UNDER 37 C.F.R. § 1.116**

Honorable Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated 18 October 2002, and concurrent with the Request for Continued Examination under 37 C.F.R. § 1.114 filed herewith, please amend the above-identified patent application as follows:

**In the Claims:**

Please substitute the following claims for the pending claims with the same claim numbers:

SUB  
E17

1. (Three Times Amended) A capacitor comprising:

a first level of at least four electrically conductive parallel lines extending in a first direction and lying in a first plane;